

```
Ti15WA (TimingServer)
#[ Keyword ][ VME @ ][ VSB @ ][ Mode ]
TIMING 0xe8ee2100 0xfffffff standalone
```

```
#[ Keyword ][ Input ] [ Freq./Div. ]
TL_INPUT TL_FREQIN 0 // Input not used
TL_OUT_SET TL_OSC_4MHz TI_OUT1 2 2.5e-07 TL_POL_HIGH 0 // default width (2MHz) (=> TI_FREQIN(1), OK)
TL_OUT_SET TL_OUT1 TI_OUT2 100 1e-06 TL_POL_HIGH 0 // (20000) (=> TI_SAMP(2), OK)
TL_OUT_SET TL_OUT2 TI_OUT3 20000 5e-05 TL_POL_HIGH 0 // default width (20000) (=> TI_FRAME(3), OK)
TL_OUT_SET TL_OUT3 TI_OUT4 32768 1 TL_POL_HIGH 0 // default width (0.6103515625) (=> TI_RUN(4), OK)
TL_OUT_SET TL_OUT2 TI_OUT5 20000 5e-05 TL_POL_HIGH 19900 // default width (2MHz) (BNC DISCONNECTED(5),NO ???)
```

```
Ti15WB (GxServer)
#[ Keyword ][ VME @ ][ VSB @ ][ Mode ]
TIMING 0xe8e82100 0xfffffff slave
```

```
#[ Keyword ] [ Input ] [ Freq./Div. ]
1TL_INPUT TI_FREQIN 2000000 // Frequency (2MHz)
2 TL_INPUT TI_SAMPLING 100 // Division compared to TI_FREQIN (20000)
3TL_INPUT TI_FRAME 20000 // Division compared to TI_SAMPLING (1)
4TL_INPUT TI_RUN 32768 // Division compared to TI_FRAME (0.6103515625)
5TL_INPUT TI_FREQIN_SYNC 2000000 // Frequency (BNC DISCONNECTED ???)
TL_OUT_SET TL_FREQIN_SYNC TI_OUT2 4000 3e-06 TL_POL_HIGH 100 // (2000000/4000=500)
TL_OUT_SET TL_FREQIN_SYNC TI_OUT3 200 5e-07 TL_POL_HIGH 100 // (2000000/200=10000)
TL_OUT_SET TL_FREQIN_SYNC TI_OUT4 200 3e-06 TL_POL_HIGH 100 // (2000000/200=10000)
TL_OUT_SET TL_FREQIN_SYNC TI_OUT5 40000 1e-05 TL_POL_HIGH 0 // (2000000/40000=50)
```

Example of suspension Timing board managed by GxServer (NorthEnd)

```
Ti76B
#[ Keyword ][ VME @ ][ VSB @ ][ Mode ]
TIMING 0xe8ee2100 0xfffffff slave
```

```
#[ Keyword ][ Input ][ Freq./Div. ]
TL_INPUT TL_FREQIN 2500000 // Frequency (2.5MHz)
TL_INPUT TL_SAMPLING 125 // Division compared to TL_FREQIN (20000)
TL_INPUT TL_FRAME 20000 // Division compared to TL_SAMPLING (1)
TL_INPUT TL_RUN 32768 // Division compared to TL_FRAME (0.6103515625)
TL_INPUT TL_FREQIN_SYNC 2500000 // Frequency (2.5MHz)
TL_OUT_SET TL_FREQIN_SYNC TI_OUT2 5000 3e-06 TL_POL_HIGH 125 // (2500000/5000=500)
TL_OUT_SET TL_FREQIN_SYNC TI_OUT3 250 4e-07 TL_POL_HIGH 125 // default width (2500000/250=10000)
TL_OUT_SET TL_FREQIN_SYNC TI_OUT4 250 3e-06 TL_POL_HIGH 125 // (2500000/250=10000)
TL_OUT_SET TL_FREQIN_SYNC TI_OUT5 50000 1e-05 TL_POL_HIGH 0 // (2500000/50000=50)
```

Conversion Code - Chart																
DECIMAL	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
HEX	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
BINARY	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111

```
DOC
1110 1110 0010 1110 0010 0001 0000 0000
e e 2 e 2 1 0 0
```

=> wrong number in the doc file

```
Ti15WB
0xe8e82100
```

- SW2(DOC):
- + 17 (+)
 - + 18 (+)
 - + 19 (+)
 - 20
 - + 21 (+)
 - 22 (+)
 - 23 (+)
 - 24
- SW1(DOC):
- + 25
 - + 26
 - + 27 (+)
 - 28
 - + 29 (+)
 - 30 (+)
 - 31 (+)
 - 32

```
11101000 11101000 0010000100000000
0010000100000000
```

```
Ti15WA
0xe8ee2100
11101000 11101110 0010000100000000
0010000100000000
```

SW2:

+ 17
+ 18
+ 19
- 20
+ 21
- 22
- 23
- 24
SW1:
+ 25
+ 26
+ 27
- 28
+ 29
+ 30
+ 31
- 32