

Minutes of Meeting

SAT Control System - Weekly Meeting

Tuesday January 31st, 2012

11.00 - 13.00

EGO - Meeting Room (Control Building)

Meeting Agenda

- Running activities status report (all)
- Control System Architecture (A.Gennai)
- Development plan (A.Gennai)
- Mode Cleaner activities (A.Gennai)

Participants

A.Gennai (AGE), C.Magazzù (CMA), D.Passuello (DPA), F.Carbognani (FCA), F.Paoletti (FPA), M.Bitossi (MBI), N.Grilli (NGI), R.Cavaliere (RCA), V.Dattilo (VDA)

Notes

The meeting was dedicated to a general presentation of the new Suspension Control System architecture aimed mainly at the preparation of the TDR and a tentative planning for the construction of a first prototypes set (slides available at https://workarea.ego-gw.it/ego2/virgo/mirror-suspension-control/advanced-virgo-suspension-control-systema/SAT_Control_System.pdf).

Statements (STA), Decisions (DEC) and Actions (ACT)

STA (FPA) "*Distribution box*" sketched in slide 9 needs to be defined.

STA (AGE-DPA) Distribution box is used to re-assemble cables in order to minimize connectors at crate backplane.

STA (VDA) Merging more than a single device (i.e. all cables required for one accelerometer) would limit devices debug

STA (MBI) Debug is not limited since we can swap boards instead of channels.

DEC (all) This hot topic requires a dedicated meeting.

ACT (AGE-Feb 3rd) Fix a meeting date for a deeper discussion about distribution box.

STA (FPA) What is the space reserved for DSP and data converters blocks?

STA (DPA) Between 1/6 and 1/3 of Eurocard.

STA (AGE) Analog section should be as small as possible in order to be able to add as many channel as possible. In case a lot of space is required we could double the height and build analog section on a separate PCB.

DEC (all) Next week meeting will be dedicated to requirements and preliminary design review for coil driver analog section.

ACT (FPA – today) Distribute already existing requirements document to prepare discussion.

ACT (AGE-Feb 3rd) Call next meeting.

STA (AGE) Software development for new DSP could start today thanks to the already available evaluation board.