



SAT Control System

The logo for VIRGO consists of a stylized teal graphic on the left, composed of several curved, parallel lines that suggest a circular or orbital path. To the right of this graphic, the word 'VIRGO' is written in a large, black, sans-serif font.

Weekly Meeting
February 14th, 2012



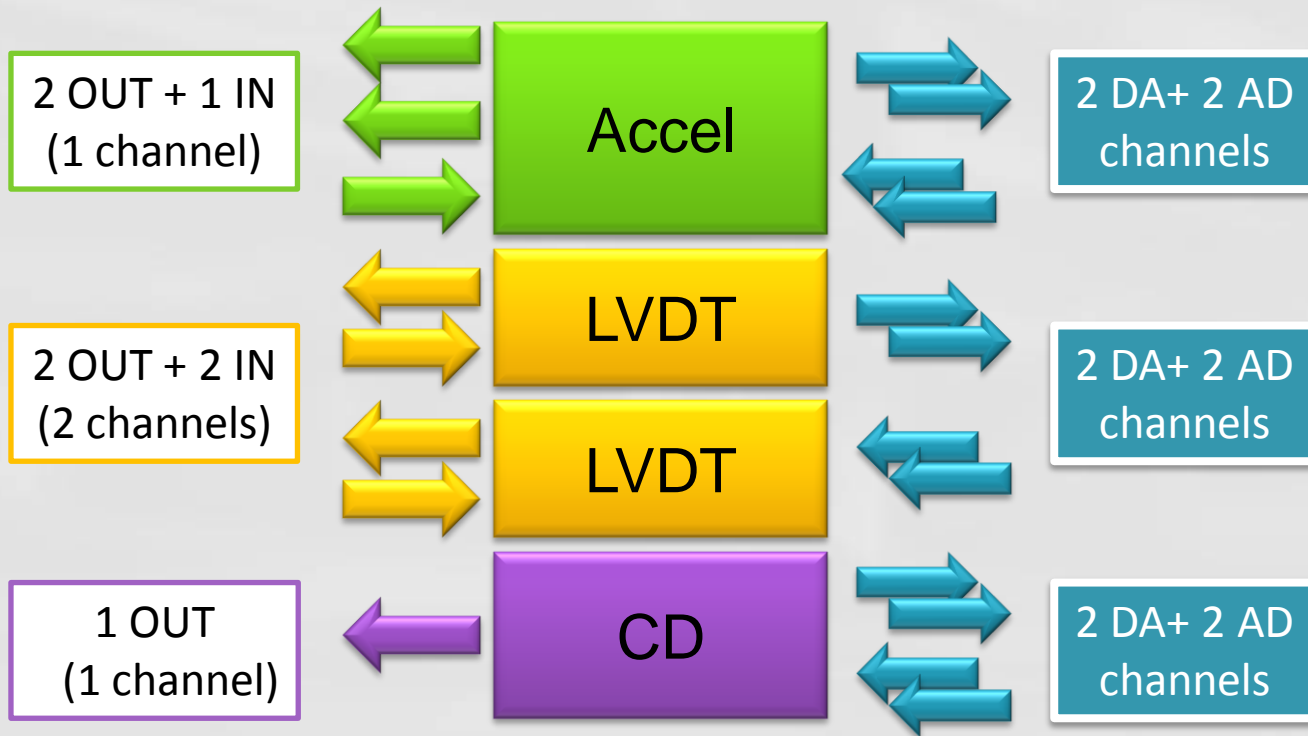
Meeting Agenda

- Approval of last meeting minutes (all - 2 minutes)
- Running activities status report (all)
- Feed through pinout: present and future (V.Dattilo)
- Suspension crates connectors pinout (R.Paoletti/A.Gennai)
- Discussion

Channels

Analog Domain

Digital Domain



Tentative Boards Configuration

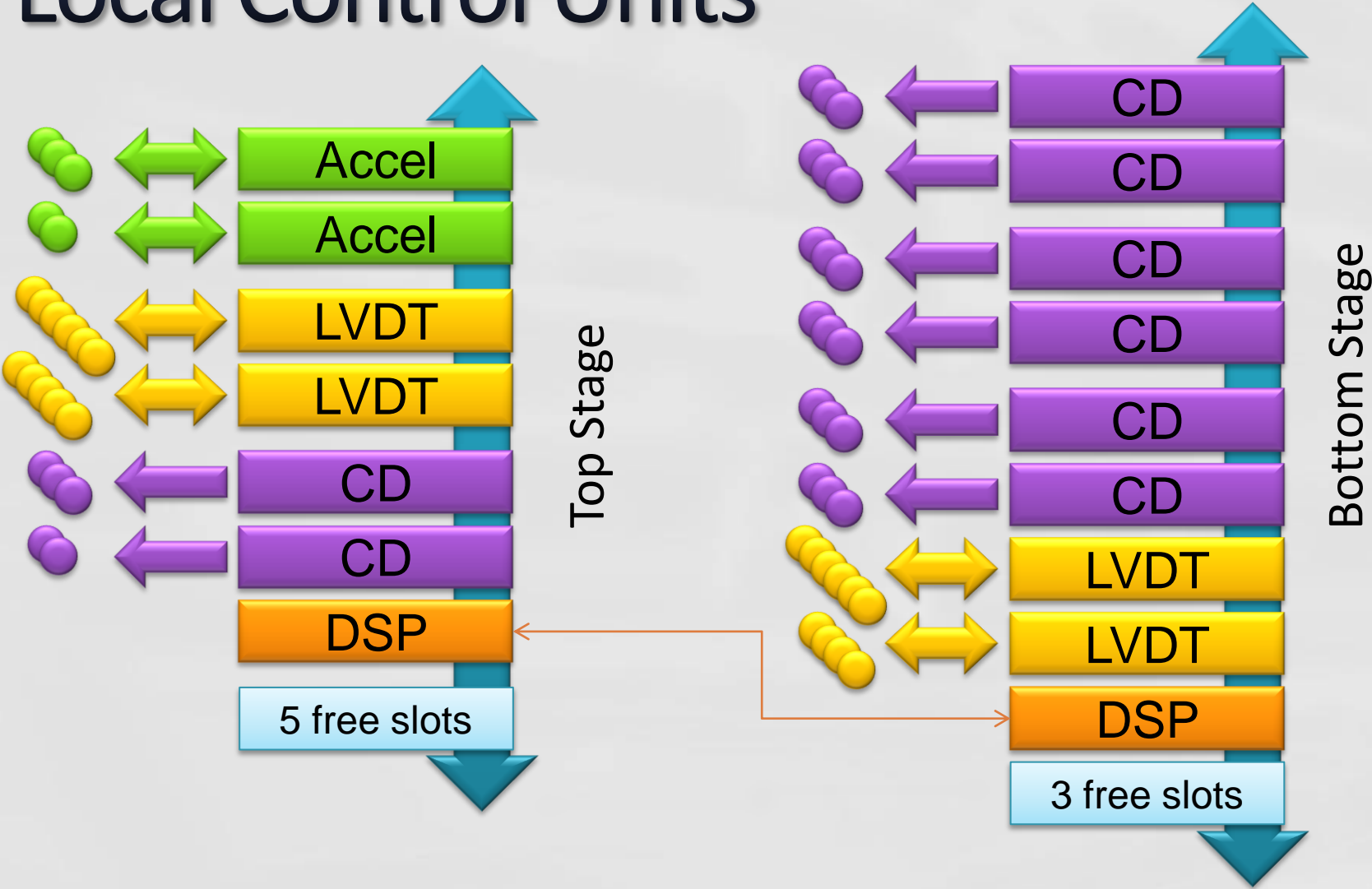


- With the following configuration all boards have the same number of ADC and DAC (6+6):
 - Accelerometer
 - 3 channels per board - 9 analog I/O – 21 to 27 pins (min one GND pin per channel max one GND pin per signal)
 - LVDT
 - 6 channels per board - 12 analog I/O (STP) - 30 to 36 pins
 - CoilDriver (2 DAC channels per coil for HP/LN)
 - 3 channels per board - 3 analog I/O (STP) - 6 to 9 pins (CD out is single ended)

SAT-PAY Signals

- Top Stage: 21 channels
 - 5 Accelerometers
 - 11 LVDT (3 Hor F#0 + 1 Vert F#0..4 + 3 Bottom Ring)
 - 5 Coils F#0
- Bottom Stage: 28 channels
 - 6 Coils F#7 control
 - 8 Coils Marionetta Control
 - 4 Coils Mirror Control
 - 6 LVDT F#7
 - 4 channels PSD (TBC)

Local Control Units



Discussion: Board - Backplane Conn

- All sites with 30-pin connector
 - 6 GND + 6 IN (12 pins) + 6 OUT (12 pins)
- Reduce LVDT channels/boards down to 4
 - One more board per Control Unit
 - All sites with 24-pin connector
- Sites connector different for different boards.
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Discussion: Backplane Connectors

