

BS TOWER

Slot3 adc #34 (90002200 - 40), slot 6 adc 37 (90002500 - 48) slot 13 adc #10 (90000a00 -40)

DAC #65	vmeaddress: 0x90014100	vbex address 40	BS ID slot 4	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	420			PP26
CH2	380			PP41
CH3	600			PP56
CH4	760			CV1
CH5	980	offset jumps		CV2
CH6	480			CH1
CH7	600			CH2
CH8	440			CH3
NOTE:				

DAC #16	vmeaddress: 0x80001000	vbex address 40	BS LC slot 11	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	505	Output offset doesn't go to zero		CH.F7.L
CH2	466			CH.F7.R
CH3	330			CH.F7.F
CH4	550			CH.F7.B
CH5	330	DAC replaced - high output noise (2u)		HC.CRM.UL
CH6	483			HC.CRM.DL
CH7	300			HC.CRM.UR
CH8	534			HC.CRM.DR
NOTE:				

DAC 12	vmeaddress: 0x8000c00	vbex address 48	BS LC slot 14	
Output sign corrected	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 07/04/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	515			LN.RM.UL
CH2	440			LN.RM.DL
CH3	500			LN.RM.UR
CH4	365			LN.RM.DR
CH5	400	DAC replaced (offset -18mV)		
CH6	390			
CH7	545			
CH8	600			
NOTE				

PR TOWER

Slot3 adc #69 (900045200 - 40), slot 6 adc 45 (90002d00 - 48) slot 13 adc #17 (90001100 - 40)

DAC #04		vmeaddress: 0x90010400	vbex address 40	PR ID slot 4	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	420				PP26
CH2	400	DAC replaced (offset -60mV) check offset			PP41
CH3	450				PP56
CH4	460				CV1
CH5	480				CV2
CH6	420				CH1
CH7	500				CH2
CH8	670	DAC replaced - high output noise (2u) check again			CH3
NOTE:					

DAC #11		vmeaddress: 0x9001b000	vbex address 40	PR LC slot 11	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	430	DAC replaced - high output noise (1u)			CH.F7.R
CH2	550				CH.F7.L
CH3	620				CH.F7.F
CH4	774				CH.F7.B
CH5	600				C.RM.R
CH6	370				C.RM.L
CH7	640				C.RM.U
CH8	740				C.RM.D
NOTE:					

NI TOWER

Slot3 adc #20 (90001400 - 40), slot 6 adc 54 (90003600 - 48) slot 13 adc #23 (90001700 -40)

DAC #49	vmeaddress: 0x90013100	vbex address 40	NI ID slot 4	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 25/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	450	DAC replaced - high output noise (.9u)		F7.COIL1 pp26
CH2	450			PR ADC5_5
CH3	440			PR ADC5_6
CH4	620			CV1
CH5	780			CV2
CH6	500			CH1
CH7	460	DAC replaced - high output noise (1.5u)		CH2
CH8	620			CH3
NOTE:				

DAC #35	vmeaddress: 0x90012300	vbex address 40	NI LC slot 11	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 25/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	492			CH.F7.L
CH2	444			CH.F7.R
CH3	400			CH.F7.F
CH4	538			CH.F7.B
CH5	546			C.RM.L
CH6	567			C.RM.R
CH7	316			C.RM.U
CH8	385			C.RM.D
NOTE:				

DAC #56	vmeaddress: 0x90013800	vbex address 48	NI LC slot 14	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 25/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	560			LN.RM.U pp39
CH2	430			LN.RM.D pp40
CH3	550			
CH4	750			
CH5	540			
CH6	740			
CH7	530			
CH8	650	DAC replaced (offset jumps)		
NOTE:				

NE TOWER

Slot3 adc #50 (90003200 - 40), slot 6 adc 33 (90002100 - 50) slot 13 adc #53 (90003500 -40)

DAC #53	vmeaddress: 0x90013500	vbex address 40	NE ID slot 4	
Output sign corrected	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	640			CH.F7.1
CH2	580			CH.F7.2
CH3	425			CH.F7.3
CH4	511			CV1
CH5	452			CV2
CH6	444	DAC replaced - high output noise (2u)		CH1
CH7	450			CH2
CH8	400			CH3
NOTE: Corrected output sign (all channels)				

DAC #52	vmeaddress: 0x90013400	vbex address 40	NE LC slot 11	
Output sign corrected	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	380			CH.F7.L
CH2	520			CH.F7.R
CH3	440			CH.F7.F
CH4	480	Verify better		CH.F7.B
CH5	500	Offset jumps, high noise hi-freq		
CH6	680			HP.RM.R
CH7	760			HP.RM.U
CH8	420			HP.RM.D
NOTE: Corrected output sign (all channels)				

DAC #34	vmeaddress: 0x90012200	vbex address 48	NE LC slot 14	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	470			LN.RM.U
CH2	600			LN.RM.D
CH3	370			
CH4	360			LN.RM.L
CH5	1000			LN.RM.R
CH6	400			HP.RM.L
CH7	540			
CH8	390			
NOTE:				

WI TOWER

Slot3 adc #50 (90003200 - 40), slot 6 adc 33 (90002100 - 50)slot 13 adc #53 (90003500 -40)

DAC #71	vmeaddress: 0x90014700	vbex address 40	WI ID slot 4	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 26/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	470			PP26
CH2	450			to BS ADC
CH3	470			to BS ADC
CH4	555			CV1
CH5	670			CV2
CH6	560			CH1
CH7	520			CH2
CH8	485			CH3
NOTE				

DAC #57	vmeaddress: 0x90013900	vbex address 40	WI LC slot 11	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 26/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	500			CH.F7.L
CH2	520			CH.F7.R
CH3	585	DAC replaced (offset jumps)		CH.F7.F
CH4	377	Replaced AA filter		CH.F7.B
CH5	440			C.RM.L
CH6	400			C.RM.R
CH7	570	DAC replaced (offset jumps)		C.RM.U
CH8	400			C.RM.D
NOTE:				

DAC #	vmeaddress: 0x90014700	vbex address 48	WI LC slot4	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1				
CH2				
CH3				
CH4				
CH5				
CH6				
CH7				
CH8				
NOTE:				

WE TOWER

Slot3 adc 65 (90004100 - 40), slot 4 adc 78 (90004e00 - 50) slot 6 adc 76 (90004c00 - 50) slot 13 adc79 (90004f00 -40)

DAC #31		vmeaddress: 0x90011f00	vbex address 40	WE ID slot 6	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100μV	Op. Code 1 17/03/2009	
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1					
CH2					
CH3					
CH4					
CH5					
CH6					
CH7					
CH8					
NOTE:					

DAC #46		vmeaddress: 0x90012e00	vbex address 40	WE LC slot 11	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100μV	Op. Code 1 17/03/2009	
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	454	DAC replaced: offset jumps			
CH2	400				
CH3	480				
CH4	475				
CH5	380				
CH6	420	DAC replaced: offset jumps			
CH7	505	DAC replaced: high noise @ 10Hz			
CH8	500				
NOTE: replaced fuse F3					

DAC #66		vmeaddress: 0x90014200	vbex address 48	WE LC slot 14	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100μV	Op. Code 1 17/03/2009	
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1					
CH2					
CH3					
CH4					
CH5					
CH6					
CH7					
CH8					
NOTE:					

IB TOWER

Slot3 adc #38 (90002600 - 40), slot 6 adc 16 (90001000 - 48) slot 13 adc #43 (90000100 -48)

DAC #25	vmeaddress: 0x80000100	vbex address 40	IB ID slot 4	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	580	DAC replaced (offset jumps)		
CH2	510			
CH3	540			
CH4	560			CV1
CH5	350			CV2
CH6	400			CH1
CH7	626			CH2
CH8	430			CH3
NOTE: check again				

DAC #70	vmeaddress: 0x90014600	vbex address 40	IB LC slot 11	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	520			S1
CH2	500			S2
CH3	700			S3
CH4	565	Offset jumps		S4
CH5	780			S5
CH6	480	DAC replaced - high output noise (3u)		S6
CH7	530	DAC replaced - high output noise (1.2u)		S7
CH8	700			S8
NOTE: check again				

DAC #51	vmeaddress: 0x90013300	vbex address 40	IB LC slot 14	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 12/05/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	377			
CH2	637			
CH3	500			
CH4	456	DAC replaced		
CH5	444			
CH6	520			
CH7	460			
CH8	420			
NOTE:				

DB TOWER

Slot3 adc #15 (80000300 - 40), slot 6 adc 19 (90001300 - 48) slot 13 adc #9 (90000000 -40)

DAC #27	vmeaddress: 0x80001b00	vbex address 40	DB ID slot 4	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1				
CH2				
CH3				
CH4				
CH5				
CH6				
CH7				
CH8				
NOTE:				

DAC #19	vmeaddress: 0x80000200	vbex address 40	DB LC slot 11	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1				
CH2				
CH3				
CH4				
CH5				
CH6				
CH7				
CH8				
NOTE:				

MC TOWER

Slot3 adc #67 (90004300 - 40), slot 6 adc60 (90003c00 - 48 slot 12 adc #55 (90003700 -40)

DAC #61	vmeaddress: 0x90013d00	vbex address 40	MC ID slot 4	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1				
CH2				
CH3				
CH4				
CH5				
CH6				
CH7				
CH8				
NOTE:				

DAC #59	vmeaddress: 0x90013b00	vbex address 40	MC LC slot 10	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1				
CH2				
CH3				
CH4				
CH5				
CH6				
CH7				
CH8				
NOTE:				

DAC #62	vmeaddress: 0x90013e00	vbex address 48	MC ID slot 13	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1				
CH2				
CH3				
CH4				
CH5				
CH6				
CH7				
CH8				
NOTE:				

AVAILABLE AS SPARE

DAC 13		vmeaddress: 0x90000d00	vbex address 40	AVAILABLE AS SPARE	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 07/04/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	400				
CH2	350				
CH3	416	AA filter replaced (jumps)			
CH4	500	AA filter replaced			
CH5	477				
CH6	395				
CH7	380	AA filter replaced			
CH8	500	AA filter replaced (jumps) DAC replaced			
NOTE:					

DAC 15		vmeaddress: 0x90010a00	vbex address 40	AVAILABLE AS SPARE	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 18/03/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	383				
CH2	457				
CH3	513	DAC replaced high noise			
CH4	460				
CH5	435				
CH6	396				
CH7	700	DAC replaced high noise			
CH8	650				
NOTE: P&P replaced (it was removed since years)					

DAC 20		vmeaddress: 0x90011400	vbex address 40	AVAILABLE AS SPARE	
Output sign corrected		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 11/06/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	450				
CH2	690				
CH3	450				
CH4	470				
CH5	630				
CH6	377				
CH7	450				
CH8	466				
NOTE: replaced clock connector					

DAC #21	vmaddress: 0x90011500	vbex address 40	AVAILABLE AS SPARE	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 18/06/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1				
CH2				
CH3				
CH4				
CH5				
CH6				
CH7				
CH8				
NOTE:				

DAC #36	vmaddress: 0x9002400	vbex address 40	AVAILABLE AS SPARE	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 02/07/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1				
CH2				
CH3				
CH4				
CH5				
CH6				
CH7				
CH8				
NOTE:				

DAC #32	vmaddress: 0x90012000	vbex address 48	AVAILABLE AS SPARE	
Output sign verified	z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 25/05/2009
	Output noise nV/sqrt(Hz)	NOTE		Device name
CH1	380	Replaced AA filter (offset jumps)		SSFS enable
CH2	520	Replaced AA filter (offset jumps)		SSFS aux
CH3	480	Replaced AA filter		SSFS ctrl
CH4	400	Replaced AA filter		PP10
CH5	580			PERTURB 65
CH6	470			
CH7	620			PP15 LN D
CH8	460			PP16 HC D
NOTE:				

DAC #33		vmeaddress: 0x80002100	vbex address 48	AVAILABLE AS SPARE	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 25/05/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	550				
CH2	530				
CH3	690	DAC replaced (offset jumps) z.c. corrected			
CH4	450	DAC replaced (offset jumps) z.c. corrected			
CH5	560	DAC replaced z.c. corrected			
CH6	440				
CH7	530				
CH8	500				
NOTE:					

DAC #43		vmeaddress: 0x90012b00	vbex address 40	AVAILABLE AS SPARE	
Output sign corrected		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 11/06/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	484				
CH2	363	DAC replaced: offset jumps			
CH3	660				
CH4	440				
CH5	480				
CH6	480	DAC replaced: high noise @ 10Hz			
CH7	340				
CH8	417				
NOTE: replaced EEPROM					

DAC #47		vmeaddress: 0x90012f00	vbex address 40	AVAILABLE AS SPARE	
Output sign corrected		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 11/06/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	588	DAC replaced: high noise @ 10Hz			
CH2	470				
CH3	554	DAC replaced: offset jumps			
CH4	546	DAC replaced: offset jumps			
CH5	540	DAC replaced: high noise @ 10Hz			
CH6	355				
CH7	450				
CH8	485				
NOTE: replaced EEPROM					

DAC #50		vmaddress: 0x90013200	vbex address 40	AVAILABLE AS SPARE	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 11/06/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	420				
CH2	590				
CH3	600				
CH4	420				
CH5	650				
CH6	400				
CH7	500				
CH8	500				
NOTE:					

DAC #68		vmaddress: 0x90014400	vbex address 40	AVAILABLE AS SPARE	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	450				
CH2	525				
CH3	558	DAC replaced: high noise @ 10Hz			
CH4	530				
CH5	590				
CH6	415	DAC replaced: gain not stable			
CH7	660				
CH8	570				
NOTE:					

DAC #72		vmaddress: 0x90014800	vbex address 40	AVAILABLE AS SPARE	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100 μ V	Op. Code 1 11/06/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	415				
CH2	370				
CH3	340				
CH4	466	DAC replaced			
CH5	530				
CH6	500				
CH7	460				
CH8	500	AA filter replaced			
NOTE: EEPROM replaced					

DAC #30		vmeaddress: 0x90011e00	vbex address 40	C34 align slot 20	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 11/06/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	490	DAC replaced high noise			
CH2	420				
CH3	445				
CH4	430				
CH5	380	DAC replaced high noise			
CH6	450	DAC replaced high noise			
CH7	410				
CH8	900				
NOTE:					

DAC #39		vmeaddress: 0x90014700	vbex address 40	ROMA (Ettore)	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	388				
CH2	570				
CH3	626				
CH4	582				
CH5	600				
CH6	500	DAC replaced: high noise @ 10Hz			
CH7	784				
CH8	460				
NOTE:					

DAC #64		vmeaddress: 0x90014000	vbex address 40	VTF	
Output sign verified		z.c. distorsion corrected	Nominal gain 2.1	Output offset below 100µV	Op. Code 1 17/03/2009
	Output noise nV/sqrt(Hz)	NOTE			Device name
CH1	471				
CH2	450				
CH3	460	DAC replaced (offset jumps)			
CH4	660				
CH5	2330	offset jumps			
CH6	500				
CH7	510				
CH8	600				
NOTE:					