



Added 22nF between pin2 & pin4 of U1 to increase stability

R2 & C2 create a pole at 1.5kHz

to GALVO (+/- 0.5A max) J2

From DAC (+/- 10V) J3 50mA/V

R6 is a series of two 1 ohm resistors

20V/A

Curent Monitor OUT

C5, C9, C7, C11 not placed

Title		
GALVO driver CH1		
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